

General Description

The MAX4634 fast, low-voltage, 4-channel CMOS analog multiplexer features 4Ω max on-resistance (R_{ON}). It offers R_{ON} matching between switches to 0.3Ω max and Ron flatness of 1Ω max over the specified signal range. Each switch can handle V+ to GND analog signals. Off-leakage current is only 0.1nA max at +25°C. The MAX4634 features fast turn-on (ton) and turn-off (tOFF) times of 18ns and 11ns, respectively. All this comes in a tiny 10-pin µMAX package.

This low-voltage multiplexer operates from a +1.8V to +5.5V single supply. All digital inputs have +0.8V and +2.4V logic thresholds, ensuring TTL/CMOS-logic compatibility with +5V operation.

Applications

Battery-Operated Equipment Audio and Video Signal Routing Low-Voltage Data-Acquisition Systems Sample-and-Hold Circuits Communications Circuits

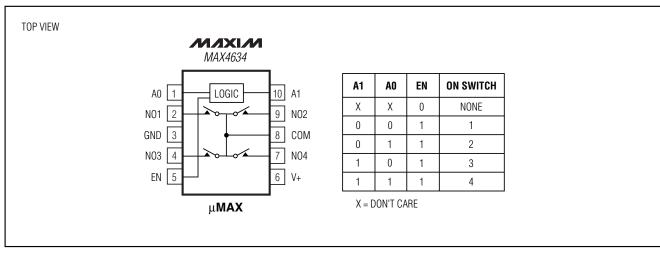
Features

- ♦ Guaranteed Ron 2.5 Ω typ (5V Supply) 4.5 Ω typ (3V Supply)
- **♦ Guaranteed Ron Match Between Channels** 0.3Ω max
- ♦ Guaranteed Ron Flatness Over Signal Range 1Ω max
- **♦** Guaranteed Low Leakage Currents 0.1nA (at +25°C)
- ♦ +1.8V to +5.5V Single-Supply Operation
- ♦ +1.8V Operation $R_{ON} = 30\Omega$ typ Over Temperature ton = 30ns typ, toff = 13ns typ
- ♦ V+ to GND Signal Handling
- **♦ TTL/CMOS-Logic Compatible**
- ◆ -78dB Crosstalk (at 1MHz)
- ♦ -80dB Off-Isolation (at 1MHz)
- ♦ 0.018% Total Harmonic Distortion

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX4634EUB	-40°C to +85°C	10 μMAX

Pin Configuration/Functional Diagram/Truth Table



MIXIM

Maxim Integrated Products 1

ABSOLUTE MAXIMUM RATINGS

(Voltages referenced to GND)	
V+	0.3V to +6V
A_, EN, COM, NO_ (Note 1)	0.3V to $(V+ + 0.3V)$
Continuous Current (all other pins)	±20mA
Continuous Current (COM, NO_)	±50mA
Peak Current (COM, NO_ pulsed at 1ms,	
10% duty cycle)	±100mA

Continuous Power Dissipation ($T_A = +70^{\circ}C$)	
10-Pin µMAX (derate 4.1mW/°C above +70°C)330mW
Operating Temperature Range	
MAX4634EUB	40°C to +85°C
Storage Temperature Range6	5°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Note 1: Signals on NO_, COM, EN, or A_ exceeding V+ or GND are clamped by internal diodes. Limit forward diode current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—Single +5V Supply

 $(V+=+4.5V \text{ to } +5.5V, V_{IH}=2.4V, V_{IL}=0.8V, T_A=-40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted.}$ Typical values are at V+ = +5V, T_A = +25^{\circ}C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
ANALOG SWITCH	•						
Analog Signal Range	V _{COM} , V _{NO} _			0		V+	V
On-Resistance	Post	$V + = 4.5V,$ $I_{COM} = 10mA,$	T _A = +25°C		2.5	4	Ω
On-nesistance	RON	$V_{NO} = 0 \text{ to V} +$	$T_A = T_{MIN}$ to T_{MAX}			4.5	52
On-Resistance Match	ADay	V+ = 4.5V, ICOM = 10mA,	T _A = +25°C		0.1	0.3	0
Between Channels (Note 3)	ΔR _{ON}	$V_{NO} = 0 \text{ to V} +$	$T_A = T_{MIN}$ to T_{MAX}			0.4	Ω
On-Resistance Flatness	Del 17(01)	AT(ON) $V+ = 4.5V,$ $I_{COM} = 10mA,$ $V_{NO} = 0 \text{ to } V+$	T _A = +25°C		0.75	1	Ω
(Note 4)	HFLAT(ON)		TA = TMIN to TMAX			1.2	
NO_ Off-Leakage	1	V+ = 5.5V; V _{COM} = 1V, 4.5V; V _{NO} = 4.5V, 1V	T _A = +25°C	-0.1	±0.01	0.1	nA
Current (Note 5)	I _{NO_(OFF)}		TA = TMIN to TMAX	-0.3		0.3	
COM Off-Leakage Current	loov voet)	V+ = 5.5V; VCOM = 1V, 4.5V;	T _A = +25°C	-0.1	±0.01	0.1	nA
(Note 5)	ICOM(OFF)	$V_{NO} = 1V, 4.5V,$ $V_{NO} = 4.5V, 1V$	$T_A = T_{MIN}$ to T_{MAX}	-0.65		0.65	na I
COM On-Leakage Current (Note 5)		V+ = 5.5V; VCOM = 1V, 4.5V;	T _A = +25°C	-0.1	±0.01	0.1	nA
	V_{NO} = 1V, 4.5V, or floating	TA = TMIN to TMAX	-0.65		0.65	1 11/4	
DIGITAL I/O (A_, EN)	1						
Input Logic High	V _{IH}			2.4			V
Input Logic Low	V _{IL}					0.8	V
Input Logic Current				-100	5	100	nA

ELECTRICAL CHARACTERISTICS—Single +5V Supply (continued)

 $(V+ = +4.5V \text{ to } +5.5V, V_{IH} = 2.4V, V_{IL} = 0.8V, T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted.}$ Typical values are at $V+ = +5V, T_A = +25^{\circ}\text{C}$.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
DYNAMIC							
Turn-On Time (Note 5)	ton	$V_{NO} = 3V$, $R_{I} = 300\Omega$,	T _A = +25°C		14	18	ns
Turr on Time (Note o)	TON	$C_L = 35pF$, Figure 2	$T_A = T_{MIN}$ to T_{MAX}			20	
Turn-Off Time (Note 5)	toff	$V_{NO} = 3V$, $R_{I} = 300\Omega$,	T _A = +25°C		6	11	ne
rum-on time (Note 3)	TOFF	C _L = 35pF, Figure 2	TA = TMIN to TMAX			13	ns
Break-Before-Make Time	t _{BBM}	$V_{NO} = 3V,$ $R_{I} = 300\Omega,$	T _A = +25°C		8		- ns
(Note 5)	rBBM	$C_L = 35pF$, Figure 3	TA = TMIN to TMAX	1			
Charge Injection	Q	V _{GEN} = 2V, R _{GEN} = 0, C _L = 5pF, Figure 4			2		рС
Off-Isolation (Note 6) V _{IS}	V _{ISO}	$C_L = 5pF, R_L = 50\Omega,$ Figure 5	f = 10MHz		-57		dB
OII-ISOIALIOIT (NOLE O)	VISO		f = 1MHz		-80		
Crosstalk (Note 7)	VCT	$C_L = 5pF, R_L = 50\Omega,$ Figure 5	f = 10MHz		-52		dB
Crossiaik (Note 1)	VCI		f = 1MHz		-78		
NO_ Off-Capacitance	C _{NO_(OFF)}	Figure 6			13		pF
COM Off-Capacitance	CCOM(OFF)	Figure 6			52		pF
COM On-Capacitance	CCOM(ON)	C _L = 5pF, Figure 6			68		pF
Total Harmonic Distortion	THD	$R_L = 600\Omega$, $f = 20Hz$ to		0.018		%	
POWER SUPPLY	1						
Power-Supply Range	V+			1.8		5.5	V
Positive Supply Current	I+	$V+ = 5.5V$, $V_{IH} = V+$, $V_{IL} = 0$			0.001	1.0	μΑ

ELECTRICAL CHARACTERISTICS—Single +3V Supply

 $(V+=+2.7V \text{ to } +3.3V, V_{IH}=2.0V, V_{IL}=0.4V, T_A=-40^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $V+=+3V, T_A=+25^{\circ}C.$) (Note 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
ANALOG SWITCH	•						
Analog Signal Range	V _{COM} _, V _{NO} _			0		V+	V
On-Resistance	Ron	V+ = 2.7V, I _{COM} = 10mA, V _{NO} = 0 to V+	T _A = +25°C		4.5	7	Ω
OIT-I lesistance	HON		TA = TMIN to TMAX			8	
On-Resistance Match	APON	V+ = 2.7V, I _{COM} = 10mA, V _{NO} = 0 to V+	T _A = +25°C		0.1	0.3	Ω
Between Channels (Note 3)	ΔRon		TA = TMIN to TMAX			0.4	

ELECTRICAL CHARACTERISTICS—Single +3V Supply (continued)

 $(V+=+2.7V \text{ to } +3.3V, V_{IH}=2.0V, V_{IL}=0.4V, T_A=-40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted.}$ Typical values are at $V+=+3V, T_A=+25^{\circ}\text{C}.)$ (Note 2)

PARAMETER	SYMBOL	COND	ITIONS	MIN	TYP	MAX	UNITS
On-Resistance Flatness	В	V+ = 2.7V,	T _A = +25°C		1.2	2.5	Ω
(Note 4)	RFLAT(ON)	$I_{COM} = 10mA,$ $V_{NO} = 0 \text{ to V} +$	TA = T _{MIN} to T _{MAX}			3	
NO_ Off-Leakage Current	1	V+ = 3.3V;	T _A = +25°C	-0.1	±0.01	0.1	
(Note 5)	INO_(OFF)	V _{COM} = 1V, 3V; V _{NO} = 3V, 1V	TA = TMIN to TMAX	-0.3		0.3	- nA
COM Off-Leakage Current	ICOM_(OFF)	V+ = 3.3V; $V_{COM} = 1V, 3V;$	T _A = +25°C	-0.1	±0.01	0.1	nA
(Note 5)	ICOM_(OFF)	V _{NO} = 3V, 1V	$T_A = T_{MIN}$ to T_{MAX}	-0.65		0.65	
COM On-Leakage Current	lager (av)	V + = 3.3V; $V_{COM} = 1V, 3V;$	T _A = +25°C	-0.1	±0.01	0.1	A
(Note 5)	ICOM_(ON)	V _{NO} _ = 1V, 3V, or floating	TA = TMIN to TMAX	-0.65		0.65	- nA
DIGITAL I/O (A_, EN)	\		1	1			
Input High	V _{IH}			2.0			V
Input Low	VIL					0.4	V
Input Logic Current				-100	5	100	nA
DYNAMIC							
Turn-On Time (Note 5)	ton	$V_{NO_{-}} = 2V,$ $C_{L} = 35pF,$ $R_{L} = 300\Omega,$ Figure 2	T _A = +25°C		16	22	ns
Turr-Ori filme (Note 3)	TON		$T_A = T_{MIN}$ to T_{MAX}			24	1115
Turn-Off Time (Note 5)	toff	V_{NO} = 2V, C_L = 35pF, R_L = 300 Ω , Figure 2	T _A = +25°C		8	14	ns
Turn-Oil Time (Note 3)	TOFF		TA = TMIN to TMAX			16	115
Break-Before-Make Time	topy	$V_{NO_{-}} = 2V$, $C_{L} = 35pF$, $R_{L} = 300\Omega$, Figure 3	T _A = +25°C		9		ns
(Note 5)	IBBM		TA = TMIN to TMAX	1			1 115
Charge Injection	Q	V _{GEN} = 1.5V, R _{GEN} = 0	0, C _L = 5pF, Figure 4		2		рС
Off Inclation (Nato 6)	\/100	$C_L = 5pF, R_L = 50\Omega,$	f = 10MHz		-57		dB
Off-Isolation (Note 6)	Viso	Figure 5	f = 1MHz		-80		
Crosstalk (Note 7)	V _{CT}	$C_L = 5pF, R_L = 50\Omega,$	f = 10MHz		-52		dB
Orossiain (Note 1)	VCI	Figure 5	f = 1MHz		-78		UD

ELECTRICAL CHARACTERISTICS—Single +3V Supply (continued)

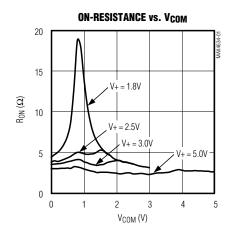
 $(V+ = +2.7V \text{ to } +3.3V, V_{IH} = 2.0V, V_{IL} = 0.4V, T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted.}$ Typical values are at $V+ = +3V, T_A = +25^{\circ}\text{C}$.) (Note 2)

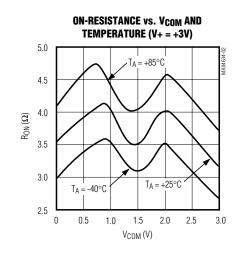
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
NO_ Off-Capacitance	CNO_(OFF)	V _{NO} _ = GND, f = 1MHz, Figure 6		13		рF
COM Off-Capacitance	CCOM(OFF)	V _{COM} = GND, f = 1MHz, Figure 6		52		рF
COM On-Capacitance	C _(ON)	V _{COM} = V _{NO} = GND, f = 1MHz, Figure 6		68		pF
Total Harmonic Distortion	THD	$R_L = 600\Omega$, $f = 20Hz$ to $20kHz$		0.018		%
POWER SUPPLY	•					
Positive Supply Current	I+	$V+ = 3.3V$, $V_{IH} = V+$, $V_{IL} = 0$		0.001	1	μΑ

- **Note 2:** The algebraic convention, where the most negative value is a minimum and the most positive value a maximum, is used in this data sheet.
- Note 3: $\Delta R_{ON} = R_{ON(MAX)} R_{ON(MIN)}$.
- **Note 4:** Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured over the specified analog signal ranges.
- Note 5: Guaranteed by design.
- Note 6: Off-Isolation = 20log₁₀ (V_{COM} / V_{NO}), where V_{COM} = output and V_{NO} = input to off switch.
- Note 7: Between any two switches.

Typical Operating Characteristics

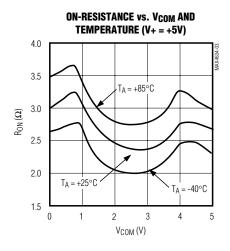
 $(T_A = +25^{\circ}C, unless otherwise noted.)$

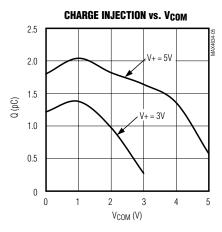


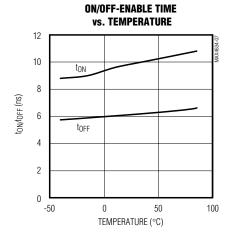


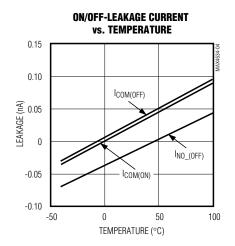
Typical Operating Characteristics (continued)

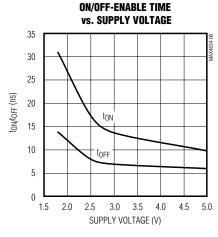
 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$

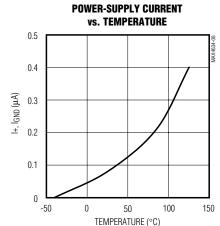






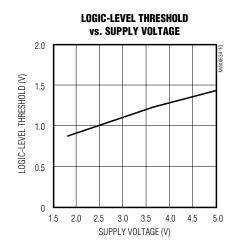


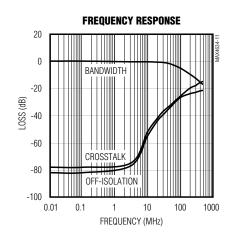




Typical Operating Characteristics (continued)

 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$





Pin Description

PIN	NAME	FUNCTION
1	A0	Address Input
2	NO1	Normally Open Switch 1
3	GND	Ground
4	NO3	Normally Open Switch 3
5	EN	Enable Logic Input
6	V+	Positive Supply Voltage
7	NO4	Normally Open Switch 4
8	СОМ	Analog Switch Common Terminal
9	NO2	Normally Open Switch 2
10	A1	Address Input

Detailed Description

The MAX4634 is a low-on-resistance, low-voltage analog multiplexer that operates from a +1.8V to +5.5V single supply. CMOS switch construction allows processing of analog signals that are within the supply voltage range (GND to V+).

To disable all switch channels, drive EN low. All four inputs and COM become high impedance during this state. If the disable feature is not needed, connect EN to V+.

Applications Information

Power-Supply Sequencing and Overvoltage Protection

Proper power-supply sequencing is recommended for all CMOS devices. Always apply V+ before applying analog signals or logic inputs, especially if the analog or logic signals are not current limited. If this sequencing is not possible, and if the analog or logic inputs are not current limited to < 20mA, add a small-signal diode (D1) as shown in Figure 1. If the analog signal can dip below GND, add D2. Adding protection diodes reduces the analog signal range to a diode drop (about 0.7V) below V+ for D1 or to a diode drop above ground for D2. The addition of diodes does not affect leakage. On-resistance increases by a small amount at low supply voltages. Maximum supply voltage (V+) must not exceed 6V.

Protection diodes D1 and D2 also protect against some overvoltage situations. A fault voltage up to the absolute maximum rating at an analog signal input does not damage the device, even if the supply voltage is below the signal voltage.

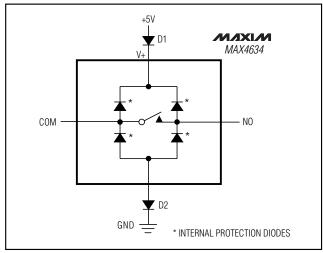


Figure 1. Overvoltage Protection Using External Blocking Diodes

Test Circuits/Timing Diagrams

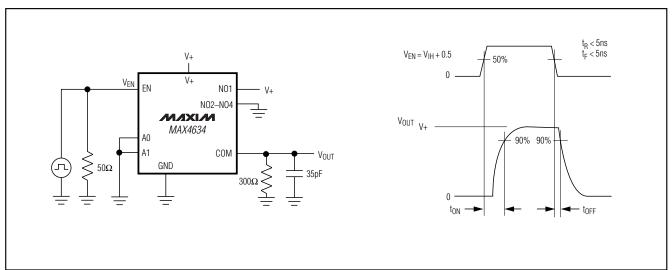


Figure 2. Switching Time

Test Circuits/Timing Diagrams (continued)

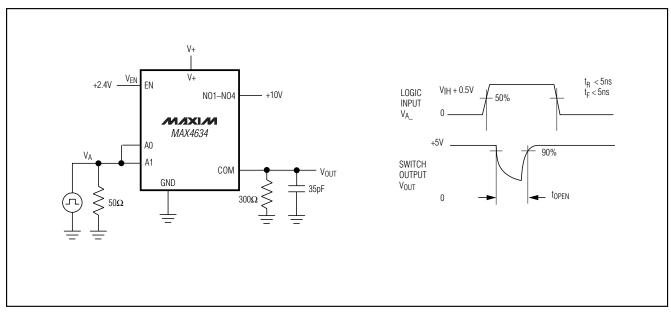


Figure 3. Break-Before-Make Interval

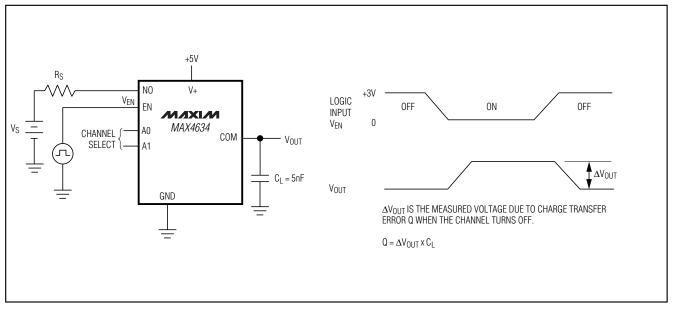


Figure 4. Charge Injection

Test Circuits/Timing Diagrams (continued)

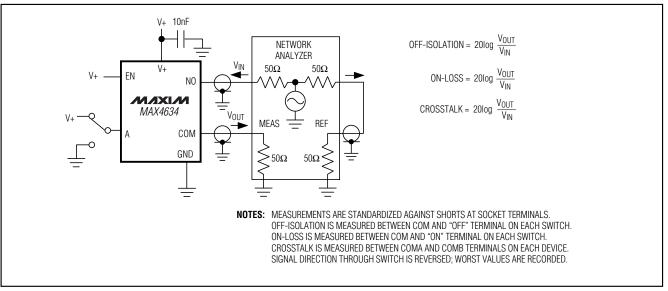


Figure 5. Off-Isolation/On-Channel Bandwidth

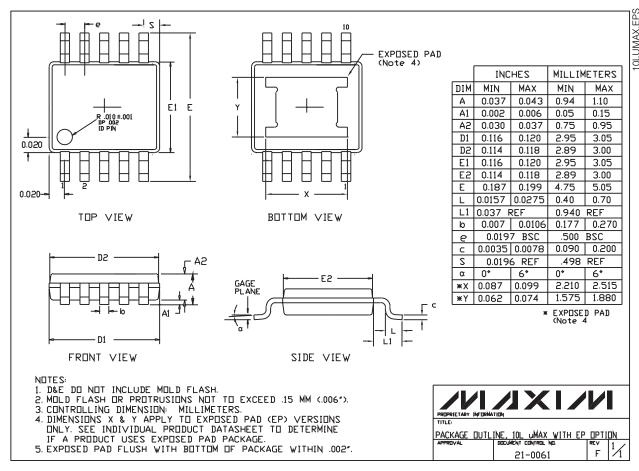
CHANNEL SELECT A1 MAX4634 N04 COM GND EN TIMHZ CAPACITANCE ANALYZER f = 1MHz

Figure 6. Channel Off/On-Capacitance

Chip Information

TRANSISTOR COUNT: 231

Package Information



Note: The MAX4634 package does not have an exposed pad.

NOTES

Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.